



Substitute for form 1449/1-78

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**Complete if Known**

Application Number	10/539,337
Filing Date	06/15/2005
First Named Inventor	John Nolan
Art Unit	2812
Examiner Name	Unknown
Attorney Docket Number	0903-004

## U. S. PATENT DOCUMENTS

[illegible]

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>2</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
/CF/		WO 02/50700 A2	06-27-2002	Claydon, et al.		
/CF/		GB 2 370 380	06-26-2000	Claydon et al.		

Examiner  
Signature

/Corey Faherty/

Date  
Considered

09/18/2007

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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE  
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(Use as many sheets as necessary)

Sheet

2

of

2

**Complete if Known**

Application Number 10/539,337

Filing Date 06/15/2005

First Named Inventor John Nolan

Art Unit 2812

Examiner Name Unknown

Attorney Docket Number 0903-004

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/CF/		ISHIJIMA et al., A Semi-Synchronous Circuit Design Method by Clock Tree Modification IEEE Trans. Fundamentals, Vol. E85-A, No. 12 December 2002	
/CF/		GREENSTREET et al., Implementing a STARI Chip, IEEE 1995	

Examiner Signature	/Corey Faherty/	Date Considered	09/18/2007
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